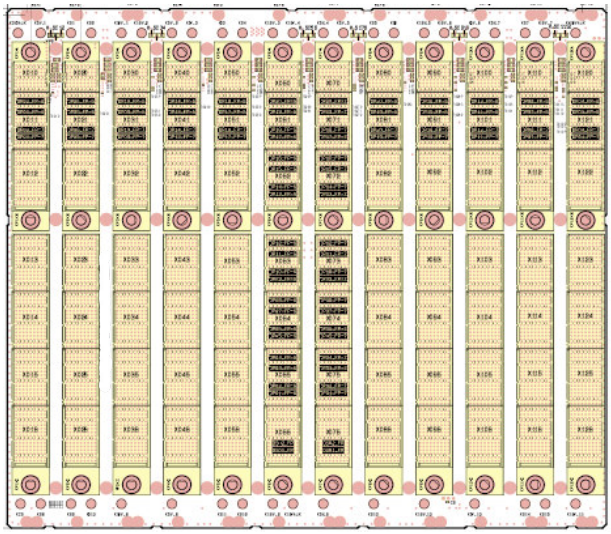
6U Open VPX BACKPLANE **BKP6-CEN12-11.2.9-3**	**VITA 46** **VITA 65**	

Key figures:

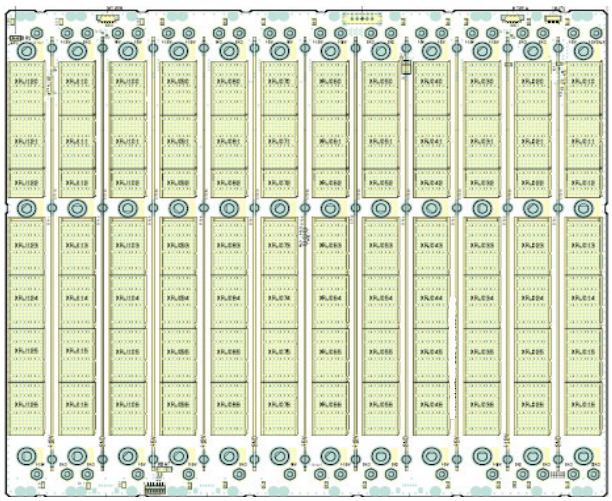
- Compliant to VITA 46.0 baseline specification
- Compilant to VITA 65 , BKP6-CEN12-11.2.9
- 12 Slots VPX, 10 Payload Slots + 2 Switch Slots
- Data plane configuration: Quad Star (4 FPs)
- Control plane configuration: 24 TPs, available to RTM
- M3 studs for main voltage power entry
- PCB size 261.85 mm x 301.65mm x 5.4 mm
- 5 HP from slot to slot (25.40 mm) 1‘‘
- Flexible keying and alignment mechanism
- with geografical address pins
- Reference clock
- Auxilary clock
- System Reset
- With JTAG connector on first slot (JT1)
- System Management Interface on the backplane (I2CA, I2CB)
- Non-Volatile Memory Read Only signal set by Jumper BR1
- Battery backup option setting by Jumper XBAT for Vbat external or connected to 3.3 AUX.
- Max. Input current per backplane :
  VS1:VS2:VS3 = 336A : 336A : 264A
- Operating temperature: -40° - +85°C
- Storage temperature: -55°C - +85°C
- Flammability rating: UL94-V0
- RoHS compliant
- Custom assembly or modification on request

- **Order number: B196102060**

Front side



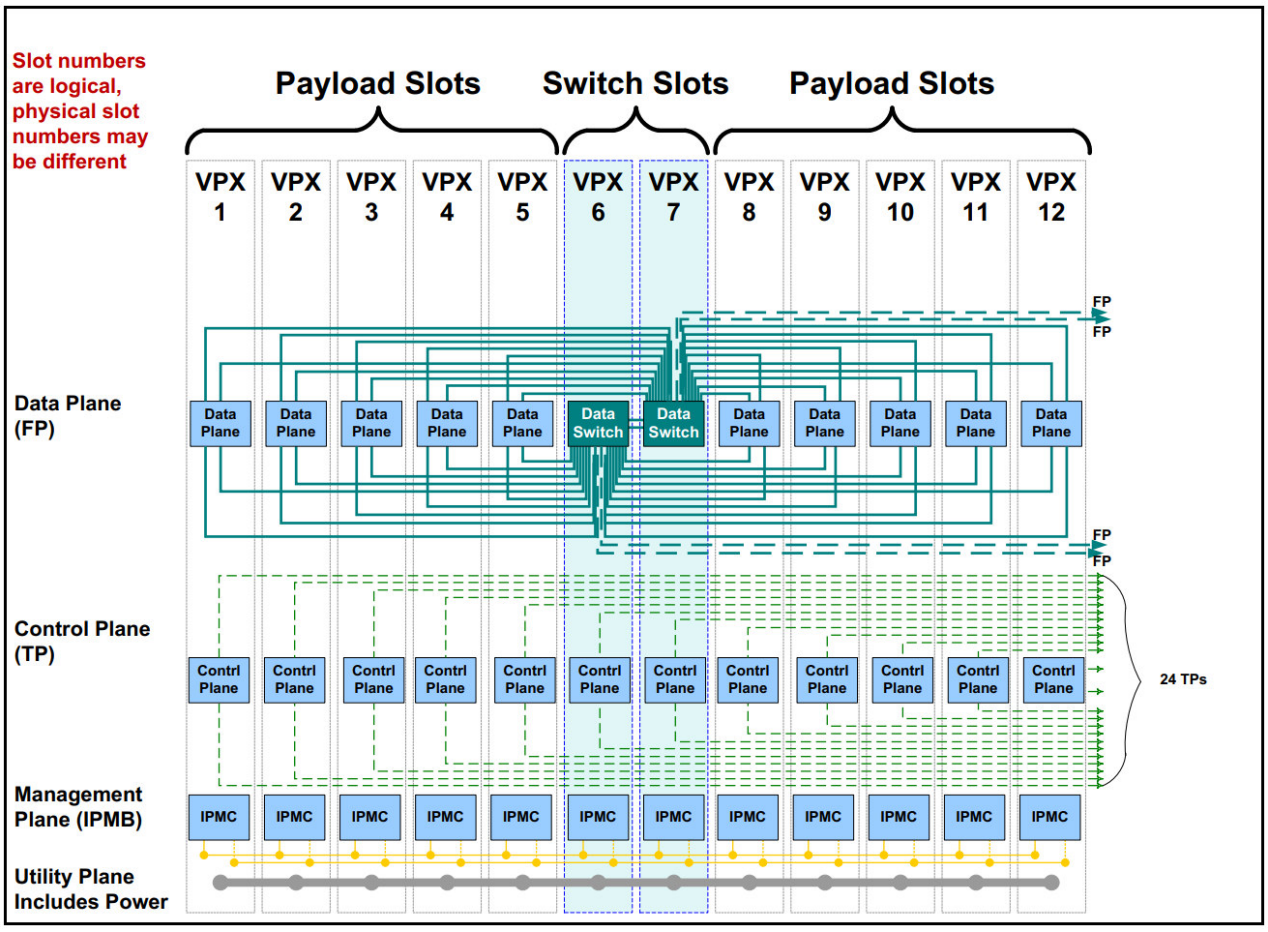
Rear side



6U Open VPX BACKPLANE **BKP6-CEN12-11.2.9-3**	**VITA 46** **VITA 65**	

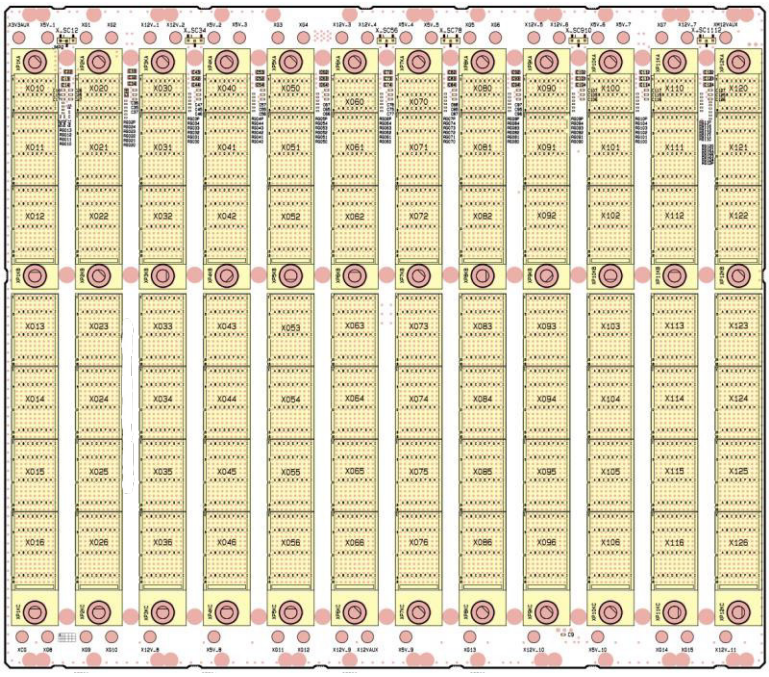
1) Topology: 12-Slot — BKP6-CEN12-11.2.9-3 (10 Payloads + 2 Switches)

Profile Payload slots: SLT6-PAY-4F2T-10.2.2
Profile Switch slots: SLT6-SWH-24F-10.4.3

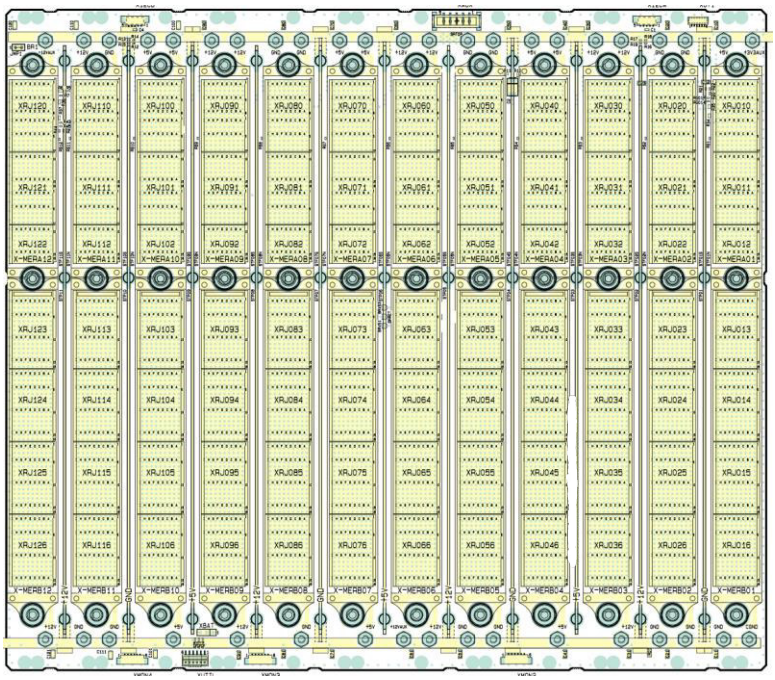


2) Drawings

Front side



Back side





3) Pin Assignment

Pin Assignment VPX J0 (Utility Connector)

	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
2	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
3	Vs3	Vs3	Vs3	Vs3	No Pad*	Vs3	Vs3	Vs3	Vs3
4	GND	SM2	SM3	GND	-12V_Aux	GND	SYSRESET*	NVMRO	GND
5	GND	GAP*	GA4*	GND	3.3V_Aux	GND	SM0	SM1	GND
6	GND	GA3*	GA2*	GND	+12V_Aux	GND	GA1*	GA0*	GND
7	TCK	GND	GND	TDO	TDI	GND	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	GND	AUX_CLK-	AUX_CLK+	GND	GND

VS1=12V , VS2=12V , VS3=5V

Payload Slot Profile SLT6-PAY-4F2T-10.2.2 — P1 & J1

Plug-In Module P1		Row G	Row F	Row E Even	Row E Odd	Row D	Row C	Row B Even	Row B Odd	Row A
Bplane J1		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Port 1	GDiscrete1	GND	*GND-J1*	DP01-T0-	DP01-T0+	GND	*GND-J1*	DP01-R0-	DP01-R0+
2		GND	DP01-T1-	DP01-T1+	*GND-J1*	GND	DP01-R1-	DP01-R1+	*GND-J1*	GND
3		P1-VBAT	GND	*GND-J1*	DP01-T2-	DP01-T2+	GND	*GND-J1*	DP01-R2-	DP01-R2+
4		GND	DP01-T3-	DP01-T3+	*GND-J1*	GND	DP01-R3-	DP01-R3+	*GND-J1*	GND
5	Data Plane Port 2	SYS_CON*	GND	*GND-J1*	DP02-T0-	DP02-T0+	GND	*GND-J1*	DP02-R0-	DP02-R0+
6		GND	DP02-T1-	DP02-T1+	*GND-J1*	GND	DP02-R1-	DP02-R1+	*GND-J1*	GND
7		Reserved	GND	*GND-J1*	DP02-T2-	DP02-T2+	GND	*GND-J1*	DP02-R2-	DP02-R2+
8		GND	DP02-T3-	DP02-T3+	*GND-J1*	GND	DP02-R3-	DP02-R3+	*GND-J1*	GND
9	Data Plane Port 3	UD	GND	*GND-J1*	DP03-T0-	DP03-T0+	GND	*GND-J1*	DP03-R0-	DP03-R0+
10		GND	DP03-T1-	DP03-T1+	*GND-J1*	GND	DP03-R1-	DP03-R1+	*GND-J1*	GND
11		UD	GND	*GND-J1*	DP03-T2-	DP03-T2+	GND	*GND-J1*	DP03-R2-	DP03-R2+
12		GND	DP03-T3-	DP03-T3+	*GND-J1*	GND	DP03-R3-	DP03-R3+	*GND-J1*	GND
13	Data Plane Port 4	UD	GND	*GND-J1*	DP04-T0-	DP04-T0+	GND	*GND-J1*	DP04-R0-	DP04-R0+
14		GND	DP04-T1-	DP04-T1+	*GND-J1*	GND	DP04-R1-	DP04-R1+	*GND-J1*	GND
15		Maskable Reset*	GND	*GND-J1*	DP04-T2-	DP04-T2+	GND	*GND-J1*	DP04-R2-	DP04-R2+
16		GND	DP04-T3-	DP04-T3+	*GND-J1*	GND	DP04-R3-	DP04-R3+	*GND-J1*	GND

6U Open VPX BACKPLANE | **VITA 46**

BKP6-CEN12-11.2.9-3 | **VITA 65**



Payload Slot Profile SLT6-PAY-4F2T-10.2.2 — P2&J2, P3&J3, P5&J5, P6&J6

This connectors are all User Defined pins. See Section 6.3.3 for requirements and pin assignments concerning connectors that are all User Defined.

Backplane Jn	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	UD	UD	UD	UD	GND	UD	UD	UD
2	GND	UD	UD	GND	UD	UD	UD	UD	GND
3	UD	UD	UD	UD	UD	GND	UD	UD	UD
4	GND	UD	UD	GND	UD	UD	UD	UD	GND
5	UD	UD	UD	UD	UD	GND	UD	UD	UD
6	GND	UD	UD	GND	UD	UD	UD	UD	GND
7	UD	UD	UD	UD	UD	GND	UD	UD	UD
8	GND	UD	UD	GND	UD	UD	UD	UD	GND
9	UD	UD	UD	UD	UD	GND	UD	UD	UD
10	GND	UD	UD	GND	UD	UD	UD	UD	GND
11	UD	UD	UD	UD	UD	GND	UD	UD	UD
12	GND	UD	UD	GND	UD	UD	UD	UD	GND
13	UD	UD	UD	UD	UD	GND	UD	UD	UD
14	GND	UD	UD	GND	UD	UD	UD	UD	GND
15	UD	UD	UD	UD	UD	GND	UD	UD	UD
16	GND	UD	UD	GND	UD	UD	UD	UD	GND

Payload Slot Profile SLT6-PAY-4F2T-10.2.2— P4 & J4

Plug-In Mod P4		Row G	Row F	Row E Even	Row E Odd	Row D	Row C	Row B Even	Row B Odd	Row A
Bplane J4		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	User Defined	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
2		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
3		UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
4		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
5		UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
6		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
7		UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
8		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
9		UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
10		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
11		UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
12		GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
13	Control Plane 2 TPs	UD	GND	GND-J4	CPtp02-DB-	CPtp02-DB+	GND	GND-J4	CPtp02-DA-	CPtp02-DA+
14		GND	CPtp02-DD-	CPtp02-DD+	GND-J4	GND	CPtp02-DC-	CPtp02-DC+	GND-J4	GND
15		UD	GND	GND-J4	CPtp01-DB-	CPtp01-DB+	GND	GND-J4	CPtp01-DA-	CPtp01-DA+
16		GND	CPtp01-DD-	CPtp01-DD+	GND-J4	GND	CPtp01-DC-	CPtp01-DC+	GND-J4	GND



Switch Slot Profile SLT6-SWH-24F-10.4.3 — P1 & J1

Plug-In Module P2		Row G	Row F	Row E Even	Row E Odd	Row D	Row C	Row B Even	Row B Odd	Row A
Bplane J2		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane FP Port 20	GDiscrete1	GND	GND-J1	DP20-T0-	DP20-T0+	GND	GND-J1	DP20-R0-	DP20-R0+
2		GND	DP20-T1-	DP20-T1+	GND-J1	GND	DP20-R1-	DP20-R1+	GND-J1	GND
3		P1-VBAT	GND	GND-J1	DP20-T2-	DP20-T2+	GND	GND-J1	DP20-R2-	DP20-R2+
4		GND	DP20-T3-	DP20-T3+	GND-J1	GND	DP20-R3-	DP20-R3+	GND-J1	GND
5	Data Plane FP Port 19	SYS_CON*	GND	GND-J1	DP19-T0-	DP19-T0+	GND	GND-J1	DP19-R0-	DP19-R0+
6		GND	DP19-T1-	DP19-T1+	GND-J1	GND	DP19-R1-	DP19-R1+	GND-J1	GND
7		Reserved	GND	GND-J2	DP19-T2-	DP19-T2+	GND	GND-J1	DP19-R2-	DP19-R2+
8		GND	DP19-T3-	DP19-T3+	GND-J1	GND	DP19-R3-	DP19-R3+	GND-J1	GND
9	Data Plane FP Port 18	UD	GND	GND-J1	DP18-T0-	DP18-T0+	GND	GND-J1	DP18-R0-	DP18-R0+
10		GND	DP18-T1-	DP18-T1+	GND-J1	GND	DP18-R1-	DP18-R1+	GND-J1	GND
11		UD	GND	GND-J2	DP18-T2-	DP18-T2+	GND	GND-J1	DP18-R2-	DP18-R2+
12		GND	DP18-T3-	DP18-T3+	GND-J1	GND	DP18-R3-	DP18-R3+	GND-J1	GND
13	Data Plane FP Port 17	UD	GND	GND-J1	DP17-T0-	DP17-T0+	GND	GND-J1	DP17-R0-	DP17-R0+
14		GND	DP17-T1-	DP17-T1+	GND-J1	GND	DP17-R1-	DP17-R1+	GND-J1	GND
15		Maskable Reset*	GND	GND-J1	DP17-T2-	DP17-T2+	GND	GND-J1	DP17-R2-	DP17-R2+
16		GND	DP17-T3-	DP17-T3+	GND-J1	GND	DP17-R3-	DP17-R3+	GND-J1	GND

Switch Slot Profile SLT6-SWH-24F-10.4.3 — P2 & J2

Plug-In Module P2		Row G	Row F	Row E Even	Row E Odd	Row D	Row C	Row B Even	Row B Odd	Row A
Bplane J2		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane FP Port 16	UD	GND	GND-J2	DP16-T0-	DP16-T0+	GND	GND-J2	DP16-R0-	DP16-R0+
2		GND	DP16-T1-	DP16-T1+	GND-J2	GND	DP16-R1-	DP16-R1+	GND-J2	GND
3		UD	GND	GND-J2	DP16-T2-	DP16-T2+	GND	GND-J2	DP16-R2-	DP16-R2+
4		GND	DP16-T3-	DP16-T3+	GND-J2	GND	DP16-R3-	DP16-R3+	GND-J2	GND
5	Data Plane Pay-load FP Port 15	UD	GND	GND-J2	DP15-T0-	DP15-T0+	GND	GND-J2	DP15-R0-	DP15-R0+
6		GND	DP15-T1-	DP15-T1+	GND-J2	GND	DP15-R1-	DP15-R1+	GND-J2	GND
7		UD	GND	GND-J2	DP15-T2-	DP15-T2+	GND	GND-J2	DP15-R2-	DP15-R2+
8		GND	DP15-T3-	DP15-T3+	GND-J2	GND	DP15-R3-	DP15-R3+	GND-J2	GND
9	Data Plane Pay-load FP Port 14	UD	GND	GND-J2	DP14-T0-	DP14-T0+	GND	GND-J2	DP14-R0-	DP14-R0+
10		GND	DP14-T1-	DP14-T1+	GND-J2	GND	DP14-R1-	DP14-R1+	GND-J2	GND
11		UD	GND	GND-J2	DP14-T2-	DP14-T2+	GND	GND-J2	DP14-R2-	DP14-R2+
12		GND	DP14-T3-	DP14-T3+	GND-J2	GND	DP14-R3-	DP14-R3+	GND-J2	GND
13	Data Plane Pay-load FP Port 13	UD	GND	GND-J2	DP13-T0-	DP13-T0+	GND	GND-J2	DP13-R0-	DP13-R0+
14		GND	DP13-T1-	DP13-T1+	GND-J2	GND	DP13-R1-	DP13-R1+	GND-J2	GND
15		UD	GND	GND-J2	DP13-T2-	DP13-T2+	GND	GND-J2	DP13-R2-	DP13-R2+
16		GND	DP13-T3-	DP13-T3+	GND-J2	GND	DP13-R3-	DP13-R3+	GND-J2	GND



Switch Slot Profile SLT6-SWH-24F-10.4.3 — P3 & J3

Plug-In Module P3		Row G	Row F	Row E		Row D	Row C	Row B		Row A
				Even	Odd			Even	Odd	
Bplane J3		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Pay-load FP Port 12	UD	GND	GND-J3	DP12-T0-	DP12-T0+	GND	GND-J3	DP12-R0-	DP12-R0+
2		GND	DP12-T1-	DP12-T1+	GND-J3	GND	DP12-R1-	DP12-R1+	GND-J3	GND
3		UD	GND	GND-J3	DP12-T2-	DP12-T2+	GND	GND-J3	DP12-R2-	DP12-R2+
4		GND	DP12-T3-	DP12-T3+	GND-J3	GND	DP12-R3-	DP12-R3+	GND-J3	GND
5	Data Plane Pay-load FP Port 11	UD	GND	GND-J3	DP11-T0-	DP11-T0+	GND	GND-J3	DP11-R0-	DP11-R0+
6		GND	DP11-T1-	DP11-T1+	GND-J3	GND	DP11-R1-	DP11-R1+	GND-J3	GND
7		UD	GND	GND-J3	DP11-T2-	DP11-T2+	GND	GND-J3	DP11-R2-	DP11-R2+
8		GND	DP11-T3-	DP11-T3+	GND-J3	GND	DP11-R3-	DP11-R3+	GND-J3	GND
9	Data Plane Pay-load FP Port 10	UD	GND	GND-J3	DP10-T0-	DP10-T0+	GND	GND-J3	DP10-R0-	DP10-R0+
10		GND	DP10-T1-	DP10-T1+	GND-J3	GND	DP10-R1-	DP10-R1+	GND-J3	GND
11		UD	GND	GND-J3	DP10-T2-	DP10-T2+	GND	GND-J3	DP10-R2-	DP10-R2+
12		GND	DP10-T3-	DP10-T3+	GND-J3	GND	DP10-R3-	DP10-R3+	GND-J3	GND
13	Data Plane Pay-load FP Port 9	UD	GND	GND-J3	DP09-T0-	DP09-T0+	GND	GND-J3	DP09-R0-	DP09-R0+
14		GND	DP09-T1-	DP09-T1+	GND-J3	GND	DP09-R1-	DP09-R1+	GND-J3	GND
15		UD	GND	GND-J3	DP09-T2-	DP09-T2+	GND	GND-J3	DP09-R2-	DP09-R2+
16		GND	DP09-T3-	DP09-T3+	GND-J3	GND	DP09-R3-	DP09-R3+	GND-J3	GND

Switch Slot Profile SLT6-SWH-24F-10.4.3 — P4 & J4

Plug-in Module P4		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J4		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Pay-load FP Port 8	UD	GND	GND-J4	DP08-T0-	DP08-T0+	GND	GND-J4	DP08-R0-	DP08-R0+
2		GND	DP08-T1-	DP08-T1+	GND-J4	GND	DP08-R1-	DP08-R1+	GND-J4	GND
3		UD	GND	GND-J4	DP08-T2-	DP08-T2+	GND	GND-J4	DP08-R2-	DP08-R2+
4		GND	DP08-T3-	DP08-T3+	GND-J4	GND	DP08-R3-	DP08-R3+	GND-J4	GND
5	Data Plane Pay-load FP Port 7	UD	GND	GND-J4	DP07-T0-	DP07-T0+	GND	GND-J4	DP07-R0-	DP07-R0+
6		GND	DP07-T1-	DP07-T1+	GND-J4	GND	DP07-R1-	DP07-R1+	GND-J4	GND
7		UD	GND	GND-J4	DP07-T2-	DP07-T2+	GND	GND-J4	DP07-R2-	DP07-R2+
8		GND	DP07-T3-	DP07-T3+	GND-J4	GND	DP07-R3-	DP07-R3+	GND-J4	GND
9	Data Plane Pay-load FP Port 6	UD	GND	GND-J4	DP06-T0-	DP06-T0+	GND	GND-J4	DP06-R0-	DP06-R0+
10		GND	DP06-T1-	DP06-T1+	GND-J4	GND	DP06-R1-	DP06-R1+	GND-J4	GND
11		UD	GND	GND-J4	DP06-T2-	DP06-T2+	GND	GND-J4	DP06-R2-	DP06-R2+
12		GND	DP06-T3-	DP06-T3+	GND-J4	GND	DP06-R3-	DP06-R3+	GND-J4	GND
13	Data Plane Pay-load FP Port 5	UD	GND	GND-J4	DP05-T0-	DP05-T0+	GND	GND-J4	DP05-R0-	DP05-R0+
14		GND	DP05-T1-	DP05-T1+	GND-J4	GND	DP05-R1-	DP05-R1+	GND-J4	GND
15		UD	GND	GND-J4	DP05-T2-	DP05-T2+	GND	GND-J4	DP05-R2-	DP05-R2+
16		GND	DP05-T3-	DP05-T3+	GND-J4	GND	DP05-R3-	DP05-R3+	GND-J4	GND



Switch Slot Profile SLT6-SWH-24F-10.4.3 — P5 & J5

Plug-In Module P5		Row G	Row F	Row E		Row D	Row C	Row B		Row A
Bplane J5		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Pay-load FP Port 4	UD	GND	GND-J5	DP04-T0-	DP04-T0+	GND	GND-J5	DP04-R0-	DP04-R0+
2		GND	DP04-T1-	DP04-T1+	GND-J5	GND	DP04-R1-	DP04-R1+	GND-J5	GND
3		UD	GND	GND-J5	DP04-T2-	DP04-T2+	GND	GND-J5	DP04-R2-	DP04-R2+
4		GND	DP04-T3-	DP04-T3+	GND-J5	GND	DP04-R3-	DP04-R3+	GND-J5	GND
5	Data Plane Pay-load FP Port 3	UD	GND	GND-J5	DP03-T0-	DP03-T0+	GND	GND-J5	DP03-R0-	DP03-R0+
6		GND	DP03-T1-	DP03-T1+	GND-J5	GND	DP03-R1-	DP03-R1+	GND-J5	GND
7		UD	GND	GND-J5	DP03-T2-	DP03-T2+	GND	GND-J5	DP03-R2-	DP03-R2+
8		GND	DP03-T3-	DP03-T3+	GND-J5	GND	DP03-R3-	DP03-R3+	GND-J5	GND
9	Data Plane Pay-load FP Port 2	UD	GND	GND-J5	DP02-T0-	DP02-T0+	GND	GND-J5	DP02-R0-	DP02-R0+
10		GND	DP02-T1-	DP02-T1+	GND-J5	GND	DP02-R1-	DP02-R1+	GND-J5	GND
11		UD	GND	GND-J5	DP02-T2-	DP02-T2+	GND	GND-J5	DP02-R2-	DP02-R2+
12		GND	DP02-T3-	DP02-T3+	GND-J5	GND	DP02-R3-	DP02-R3+	GND-J5	GND
13	Data Plane Pay-load FP Port 1	UD	GND	GND-J5	DP01-T0-	DP01-T0+	GND	GND-J5	DP01-R0-	DP01-R0+
14		GND	DP01-T1-	DP01-T1+	GND-J5	GND	DP01-R1-	DP01-R1+	GND-J5	GND
15		UD	GND	GND-J5	DP01-T2-	DP01-T2+	GND	GND-J5	DP01-R2-	DP01-R2+
16		GND	DP01-T3-	DP01-T3+	GND-J5	GND	DP01-R3-	DP01-R3+	GND-J5	GND

Switch Slot Profile SLT6-SWH-24F-10.4.3 — P6 & J6

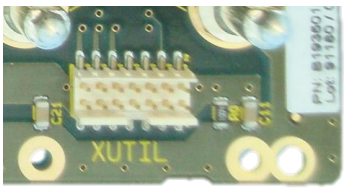
Plug-In Module P6		Row G	Row F	Row E Even	Row E Odd	Row D	Row C	Row B Even	Row B Odd	Row A
Bplane J6		Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Data Plane Inter-SW FP Port 4	UD	GND	GND-J6	DS04-T0-	DS04-T0+	GND	GND-J6	DS04-R0-	DS04-R0+
2		GND	DS04-T1-	DS04-T1+	GND-J6	GND	DS04-R1-	DS04-R1+	GND-J6	GND
3		UD	GND	GND-J6	DS04-T2-	DS04-T2+	GND	GND-J6	DS04-R2-	DS04-R2+
4		GND	DS04-T3-	DS04-T3+	GND-J6	GND	DS04-R3-	DS04-R3+	GND-J6	GND
5	Data Plane Inter-SW FP Port 3	UD	GND	GND-J6	DS03-T0-	DS03-T0+	GND	GND-J6	DS03-R0-	DS03-R0+
6		GND	DS03-T1-	DS03-T1+	GND-J6	GND	DS03-R1-	DS03-R1+	GND-J6	GND
7		UD CPtp02-DB+	GND	GND-J6	DS03-T2-	DS03-T2+	GND	GND-J6	DS03-R2-	DS03-R2+
8		GND	DS03-T3-	DS03-T3+	GND-J6	GND	DS03-R3-	DS03-R3+	GND-J6	GND
9	Data Plane Inter-SW FP Port 2	UD	GND	GND-J6	DS02-T0-	DS02-T0+	GND	GND-J6	DS02-R0-	DS02-R0+
10		GND	DS02-T1-	DS02-T1+	GND-J6	GND	DS02-R1-	DS02-R1+	GND-J6	GND
11		UD	GND	GND-J6	DS02-T2-	DS02-T2+	GND	GND-J6	DS02-R2-	DS02-R2+
12		GND	DS02-T3-	DS02-T3+	GND-J6	GND	DS02-R3-	DS02-R3+	GND-J6	GND
13	Data Plane Inter-SW FP Port 1	UD	GND	GND-J6	DS01-T0-	DS01-T0+	GND	GND-J6	DS01-R0-	DS01-R0+
14		GND	DS01-T1-	DS01-T1+	GND-J6	GND	DS01-R1-	DS01-R1+	GND-J6	GND
15		UD	GND	GND-J6	DS01-T2-	DS01-T2+	GND	GND-J6	DS01-R2-	DS01-R2+
16		GND	DS01-T3-	DS01-T3+	GND-J6	GND	DS01-R3-	DS01-R3+	GND-J6	GND

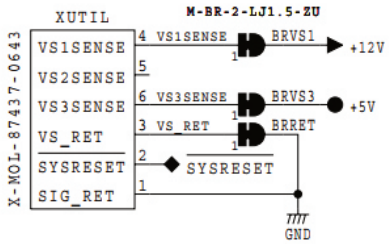
6U Open VPX BACKPLANE	VITA 46	
BKP6-CEN12-11.2.9-3	VITA 65	

4) Current Capability:

- +12V 336 A
- +12V 336 A
- +5V 264 A
- -12V AUX 12 A
- +12V AUX 12 A
- +3.3V AUX 12 A

5) UTILITY (Connector XUTIL)



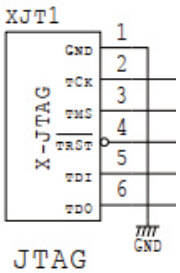


Connector Molex Art. Nr. 87437-0643

6) JTAG (Connector XJT1)



Consider: JTAG only at Slot 1, Payload slot



Connector J.S.T. BM06B-SRSS-TB(LF)(SN)

7) SYSCON

By setting the signal Syscon to GND the system slot is defined. In general the system slot is slot 1.

There are additional connectors X_SC34, X_SC56, X_SC78 and X_SC910 so as you can select any slot as system slot

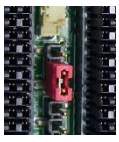
We offer 2 options for setting:

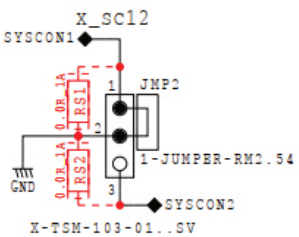
- Jumper (standard)

- 0 Ohm Resistor for rugged applications

X_SC12

1	SYSCON1
2	GND
3	SYSCON2





6U Open VPX BACKPLANE	VITA 46	
BKP6-CEN12-11.2.9-3	VITA 65	

8) I2C Connector

There are 2 connectors for system-management I2CA and I2CB.

For customer specific board assembly Zero-Ohm resistors available.

Usable voltages for I2C are 3.3V-AUX

I2CA

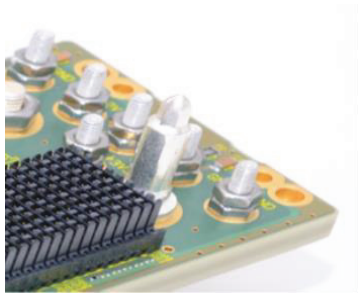
1	I2CA_SCL
2	GND
3	I2CA_SDA
4	I2CA_PWR
5	NC

I2CB

1	I2CB_SCL
2	GND
3	I2CB_SDA
4	I2CB_PWR
5	NC

9) Power Studs M3

The main operating voltages (+12V, +5V) and GND are supplied with M3 screw terminals. The auxiliary operating voltages are supplied via M3 screw terminals. Optimal daughter board supply and trouble-free operation are ensured by the arrangement of the feed modules on the backplane.

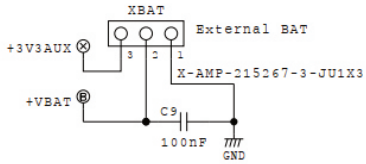


10) XBAT

Normally a battery voltage with approximately 3V is available at Pin VBAT of connector VPX-J1. The voltage is externally accessible with connector XBAT, Pin2 **or** internally using 3.3V_AUX by setting a Jumper between Pin2 and Pin3.

XBAT

1	GND
2	+VBAT
3	+3.3V_AUX

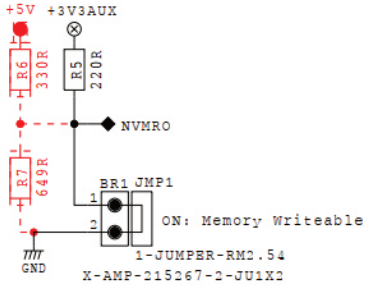


11) NVMRO

If Jumper BR1 is closed NVRMO is set to memory writeable.

BR1

1	NVMRO
2	GND



All data is for information purposes only and not guaranteed for legal purposes. Information has been carefully checked and is believed to be accurate; however, no responsibility is assumed for inaccuracies. Specifications are subject to change without notice.